

**C. Conclusion**

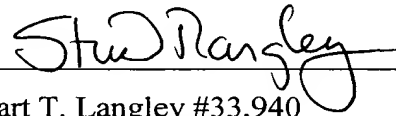
In view of all of the above claims 1-35 are believed to be allowable and the case in condition for allowance which action is respectfully requested. The references that were cited and not relied upon are believed to be no more pertinent than those references that were relied upon.

No fee is believed to be required by this response as determined on the accompanying transmittal letter. Should any other fee be required, please charge Deposit 50-1123. This response is filed together with a request for a one month extension of time and the required fee of \$110. Should any additional extension of time be required please consider this a petition therefore and charge the required fee to Deposit Account 50-1123. Attached hereto is a marked-up version of the changes made to the specification and claims by the current amendment. The attached page is captioned "**Version With Markings To Show Changes Made**"

Respectfully submitted,

Date: February 24, 2003

BY: \_\_\_\_\_



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**VERSION WITH MARKINGS TO SHOW CHANGES MADE**

**A. In the claims**

1(Twice Amended). In a system comprising an interconnect and a plurality of modules connected to said interconnect for putting packets of information onto the interconnect, wherein each packet comprises a number of fields containing information including packet routing information, a circuit comprising:

circuitry for receiving at least part of said information;

circuitry for determining if said at least part of said information satisfies one or more conditions; and

circuitry for performing one or more actions in response to the determination that at least part of the information satisfies one or more conditions.

22(Twice Amended). A functional circuit comprising:

an interconnect;

one or more modules connected to the interconnect; and

a monitoring circuit for monitoring information containing packets put onto the interconnect by one or more modules, said information-containing packets including packet routing information and said monitoring circuit comprising:

circuitry for determining if the information in a packet matches one or more conditions; and

circuitry for performing one or more actions if it is determined that information on the interconnect matches said one or more conditions.

33(Twice Amended). A method comprising the steps of:

monitoring information containing packets on an interconnect, the information being put onto the interconnect by one or modules, said information-containing packets including packet routing information;

determining if the information on an interconnect satisfies one or more conditions; and

carrying out one or more actions if it is determined that the information containing packet satisfies one or more conditions.

34(Twice Amended). A circuit for monitoring packet information on an interconnect, said packet information including packet routing information and said packet information being put onto the interconnect by one or more modules connected to the interconnect, said circuit being arranged to determine if the information satisfies one or more conditions.

35(Twice Amended). A circuit for monitoring packet information on an interconnect, said packet information including packet routing information and said packet information being put onto the interconnect by one or more modules connected to the interconnect, said circuit being arranged to determine if the information satisfies one or more conditions and to select the information satisfying the one or more conditions.

36(NEW). A circuit in a system comprising an interconnect and a plurality of modules connected to said interconnect for putting packet-format information onto the interconnect, wherein said interconnect is not a circuit-switched bus, wherein each packet comprises a number of fields containing information, including a routing field, an address field, a source field, a transaction type field, a transaction identifier field, and an operation code field, the circuit comprising:

circuitry for receiving at least part of said information from at least one of said fields;

circuitry for determining if said at least part of said information satisfies one or more conditions; and

circuitry for performing one or more actions in response to the determination that at least part of the information satisfies one or more conditions.